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Gernot Koch, Udo Kebschull, Wolfgang Rosenstiel

September 1994 **Proceedings of the 3rd international workshop on Hardware/software co-design**

Full text available:  pdf(516.50 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

We present a prototyping environment with special benefit for hardware/software codesign which we use as target architecture in the COBRA project. This architecture is very flexible, easy extensible, and provides a high gate complexity. It supports standard processor integration as well as processor emulation.

2 [ICEBERG: an embedded in-circuit emulator synthesizer for microcontrollers](#)

Ing-Jer Huang, Tai-An Lu

June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation**

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3 [The case for a configure-and-execute paradigm](#)

Frank Vahid, Tony Givargis

March 1999 **Proceedings of the seventh international workshop on Hardware/software codesign**


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Keywords: IP, cores, methodology, system-on-a-chip

4 [Fast development of source-level debugging system using hardware emulation \(short paper\)](#)

Sang-Joon Nam, Jun-Hee Lee, Byoung-Woon Kim, Yeon-Ho Im, Young-Su Kwon, Kyong-Gu Kang, Chong-Min Kyung

January 2000 **Proceedings of the 2000 conference on Asia South Pacific design automation**

Full text available:  pdf(204.60 KB) Additional Information: [full citation](#), [references](#)

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Kurt Keutzer

February 1997 **Proceedings of the 1997 ACM fifth international symposium on Field-**

programmable gate arrays

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6 [Linking codesign and reuse in embedded systems design](#)

M. Meerwein, C. Baumgartner, W. Glauert

May 2000 **Proceedings of the eighth international workshop on Hardware/software codesign**


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This paper presents a complete codesign environment for embedded systems which combines automatic partitioning with reuse from a module database. Special emphasis has been put on satisfying the requirements of industrial design practice and on the technical and economic constraints associated with automotive control applications. The object-oriented database architecture allows efficient management of a large number of modules. Experimental results from a real-world example demonstr ...

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Sungjoo Yoo, Jong-Eun Lee, Jinyong Jung, Kyungseok Rha, Youngchul Cho, Kiyong Choi

January 2000 **Proceedings of the conference on Design, automation and test in Europe**

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8 [A three-step approach to the functional partitioning of large behavioral processes](#)

Frank Vahid

December 1998 **Proceedings of the 11th international symposium on System synthesis**


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9 [A Hardware/Software Concurrent Design for a Real-Time SP@ML MPEG2 Video-Encoder Chip Set](#)

Mitsuo Ikeda, Tsuneo Okubo, Tetsuya Abe, Yoshinori Ito, Yutaka Tashiro, Ryota Kasai

March 1996 **Proceedings of the 1996 European conference on Design and Test**





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